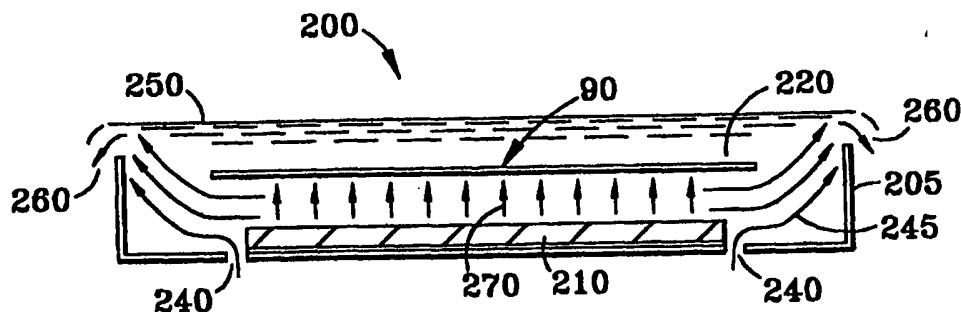


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INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification 6 : B08B 3/12, H01L 21/00	A1	(11) International Publication Number: WO 00/21692 (43) International Publication Date: 20 April 2000 (20.04.00)
(21) International Application Number: PCT/US99/02686 (22) International Filing Date: 8 February 1999 (08.02.99) (30) Priority Data: 60/104,131 14 October 1998 (14.10.98) US (71)(72) Applicant and Inventor: BUSNAINA, Ahmed, A. [US/US]; 6 Sealy Drive, Potsdam, NY 13676 (US). (74) Agent: TOUW, Theodore, R.; 4 Forest Lane, Westford, VT 05494 (US).	(81) Designated States: AU, CA, CN, IL, JP, KR, MX, NZ, RU, SG, US, Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE). Published <i>With international search report.</i>	

(54) Title: FAST SINGLE-ARTICLE MEGASONIC CLEANING PROCESS



(57) Abstract

A fast single-article megasonic cleaning system (200) is used to clean substrates (such as semiconductor wafers, flat panel display glass, etc.) at frequencies of 400 kHz–20,000 kHz or higher. The technique provides a single-wafer cleaning process that reduces the cleaning time from the 10–20 minutes typical of the prior art to 15–60 seconds. The system utilizes concentrated megasonic energy on one wafer (90) to dramatically reduce cleaning time. The system uses a transducer (210) parallel to the substrate (90) to be cleaned where the transducer area is more than about 40 % of the substrate area. Two alternate configurations are disclosed, one utilizing a horizontal wafer arrangement and the second utilizing a vertical wafer arrangement. The latter requires a smaller floor area. Preferred spacings between the wafer and the transducer, preferred transducer power and intensity, preferred overflow flow rate of fluid medium (220) (which may be deionized water), effective cleaning times, and process temperature are disclosed.

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Title of Invention**Fast Single-Article Megasonic Cleaning Process**

5

Technical Field

This invention relates generally to surface cleaning of articles such as semiconductor wafers, flat panel display glass, hard disk drives and heads, and the like to remove particulate and chemical contaminants. In particular, the invention relates to

10 megasonic cleaning of oxide, metallic, or polymer films following planarization (Chemical Mechanical Polishing, CMP) and other polishing processes.

Background Art

Wafer cleaning (especially megasonic wafer cleaning) is used before and after most

15 basic semiconductor manufacturing processes such as: pre-oxidation, pre-CVD, pre-EPI, post-ASH, and post-CMP. Megasonic cleaning is used in every major semiconductor fabrication facility today. The majority of these processes are batch processes. A paper by G. W. Gale and A. A. Busnaina, "Removal of particle contaminations using ultrasonics and megasonics: a review", Particulate Science and

20 Technology, vol. 13, pp. 197 – 211 (1995) reviewed the background art. Some megasonic nozzles are being marketed for rinsing purposes after contact cleaning processes. Such nozzles are available from Dainippon Screen Mfg. Co. of Kyoto, Japan; Solid State Equipment Corp. of Fort Washington, PA, and others. However, available megasonic nozzles are not sufficiently effective in cleaning wafers because

25 of the low power, the low flow rate, and their small relative size (a small ratio of transducer area to wafer area). No effective, fast, non-contact, single-wafer cleaning process exists today. One company (Verteq, Inc., of Santa Ana, CA) has produced a megasonic single-wafer cleaning system called "Goldfinger." The Goldfinger system uses one transducer above a rotating wafer, with a meniscus between the wafer and

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the transducer. Single-wafer megasonic cleaning methods and apparatus are described in U.S. Pat. Nos. 5,090,432, 5,148,823, and 5,286,657 to Bran.

Most megasonic cleaning tanks are employed in batch cleaning processes that may take, on average, between 10 and 20 minutes for cleaning a batch of about 25 wafers.

- 5 The long cleaning time has been a major problem and a source of low production output. In addition, the majority of other semiconductor manufacturing processes are single-wafer processes. Therefore, use of a batch process for cleaning creates bottleneck and other wafer-handling problems associated with integrating the vast number of single-wafer manufacturing processes with batch cleaning processes.
- 10 Megasonic single-wafer cleaning systems using a relatively small transducer above a rotating wafer with a meniscus between the wafer and the transducer have been extensively tested by users and have not been proven sufficiently effective. Therefore they have not been generally adopted for demanding cleaning applications such as post-Chemical-Mechanical-Polishing (CMP) cleaning in the semiconductor and other
- 15 industries. Reasons for the insufficient effectiveness include the facts that the megasonic energy delivered per square centimeter of the wafer is very small in such systems and that the megasonic energy is delivered for a fraction of the time during the duration of the cleaning process. These limitations diminish the cleaning effectiveness of the process.

20

Objects and Advantages

Accordingly, several objects and advantages of the present invention are to provide surface cleaning of semiconductor wafers, flat-panel-display glass, or hard-disk-drive disks and heads effectively in a very short time, with or without the use of chemicals

- 25 other than deionized (DI) water. The process of the present invention has improved effectiveness and efficiency in comparison with all of the cleaning process products of the prior art. The cleaning system of the present invention, which is compact in size and process, puts an end to all the problems associated with batch cleaning processes. The improvements are accomplished in part by providing a new apparatus and process
- 30 that utilizes a different design geometry than those commonly used in megasonic

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cleaning tanks. The improvements also involve system and process specifications such as the relative size of the transducer area with respect to the substrate to be cleaned (e.g., semiconductor wafers), the distance between wafer and transducer, the transducer power and intensity, the overflow flow rate, the cleaning time, and the

5 process temperature. A particular advantage of the present invention is that maximum megasonic energy is delivered to every square centimeter of the wafer area for the entire duration of the cleaning process without the need for wafer rotation. Two alternate configurations are presented; one uses a smaller foot print to reduce the floor area that the tool will occupy. Experimental data shows that the cleaning efficiency

10 obtained using this process (in less than one minute, and often as little as 15 seconds) is better than that of a batch megasonic cleaning after 13 minutes. A key factor is in the application of the same amount of megasonic energy to one wafer in the present invention as is used in cleaning 25 wafers in methods of the prior art. Still further objects and advantages will become apparent from a consideration of the ensuing

15 description and accompanying drawings.

With the recent trend by semiconductor manufacturers of adopting single-wafer processes in manufacturing, the improved process of the present invention is expected to reduce cleaning and manufacturing time and is expected to solve the bottleneck and other wafer-handling problems associated with integrating the vast number of single-

20 wafer manufacturing processes with cleaning processes.

As pointed out above, most batch cleaning megasonic cleaning processes take, on average, 10 - 20 minutes time for cleaning a batch of about 25 semiconductor wafers, hard-drive disks, or flat-panel-display glass substrates. Most attempts by various equipment manufacturers at cleaning a single wafer in a short time using a megasonic

25 process have not been successful. There is an immediate need for an effective, fast, non-contact, single-wafer cleaning method especially for post-chemical-mechanical-polishing (post-CMP) cleaning applications.

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Brief Description of the Drawings

- FIG. 1. Schematic cross-sectional elevation view of a typical batch megasonic cleaning apparatus of the prior art.
- 5 FIG. 1A. Magnified detail of a portion of FIG. 1.
- FIG. 2. Schematic cross-sectional elevation view of a fast single-wafer megasonic cleaning apparatus made in accordance with the present invention.
- FIG. 3. Schematic cross-sectional elevation view of a second embodiment of a fast single-wafer megasonic cleaning apparatus made in accordance with the present invention.
- 10 FIG. 4. Bar chart illustrating removal efficiency of silica particles using apparatus and methods of the present invention.
- FIG. 5. Bar chart illustrating removal efficiency of alumina particles using the present invention, compared with removal efficiency using a method and apparatus of the prior art for various cleaning times.
- 15 FIG. 6. Bar chart illustrating particle counts of alumina particles before and after deposition, and after cleaning by use of the present invention in comparison with particle counts with a prior art method and apparatus.

20 Reference numerals used in the drawings

The following list identifies elements designated by the reference numerals in the drawings:

- 10 Typical batch megasonic cleaning apparatus of the prior art.
- 20 Multiplicity of wafers to be cleaned
- 25 30 Cassette for holding wafers 20
- 40 Container for holding cassette of wafers

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- 50 Liquid cleaning medium
- 55 Surface of liquid cleaning medium
- 60 Megasonic transducer
- 70 Megasonic energy transfer
- 5 90 Individual wafer to be cleaned
- 100 Flow of liquid past surfaces of individual wafer 90
- 200 Apparatus for fast single-wafer megasonic cleaning process of this invention
- 205 Container for holding single wafer 90 to be cleaned and liquid cleaning medium
- 220
- 10 210 Megasonic transducer
- 220 Liquid cleaning medium
- 240 Liquid inlet flowlines
- 245 Liquid flow lines within container
- 250 Free surface of liquid
- 15 260 Liquid outlet flowlines (overflow)
- 270 Megasonic energy transfer directed toward surface of single wafer 90
- 400 Particle removal efficiency (vertical axis)
- 410 Time in seconds (horizontal axis)
- 420 Efficiency of removing 0.15 micrometer silica particles with 15 sec. cleaning
- 20 430 Efficiency of removing 0.15 micrometer silica particles with 30 sec. cleaning
- 440 Efficiency of removing 0.15 micrometer silica particles with 45 sec. cleaning
- 500 Particle removal efficiency (vertical axis)
- 510 Process used (horizontal axis)
- 520 Efficiency of removing alumina particles with 10 min. cleaning using batch
- 25 megasonic process of the prior art

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- 530 Efficiency of removing alumina particles with 20 min. cleaning using batch megasonic process of the prior art
- 540 Efficiency of removing alumina particles with 1 min. cleaning using the fast single-wafer megasonic process of the present invention
- 5 600 Number of particles larger than 0.1 micrometer (vertical axis)
- 610 Process used (horizontal axis)
- 620 Before deposition
- 630 After deposition
- 640 After cleaning
- 10 650 Cleaning by batch megasonic cleaning process of the prior art for 10 min.
- 660 Cleaning by batch megasonic cleaning process of the prior art for 20 min.
- 670 Cleaning by fast single-wafer cleaning process of the present invention for 1 min.

Disclosure of Invention

- 15 A megasonic transducer is used to clean substrates (such as semiconductor wafers) at frequencies larger than 400 kHz - 20,000 kHz or higher. The technique introduces a single-wafer cleaning process that reduces the cleaning time from 10 - 20 minutes to the present invention's cleaning time of 15 - 60 seconds. The process of the present invention cleans a wafer in less than one minute without utilizing any chemistry other
- 20 than deionized (DI) water. The use of chemistry such as SC1 (5 - 40 H₂O: 1 - 2 H₂O₂: 1 NH₄OH) should reduce the current cleaning time. Megasonic cleaning provides a very small acoustic boundary layer (on the order of 0.59 microns for 900 kHz) which exposes contaminants, such as submicron particles, to the fluid's acoustic stream and facilitates their removal. It has been shown that the new process is capable of
- 25 completely removing particles as small as 100 nanometers (current surface detection limits). The detailed description below indicates why current megasonic equipment used today is not capable of matching the current removal efficiency provided by this invention using the same cleaning time. The semiconductor industry is quickly

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moving toward single-wafer processing. Today more than 80% of wafer processing is single-wafer based. This process eliminates the need for batch cleaning processes as well as reducing the time per wafer to less than 60 seconds. The cleaning time depends on the type of contaminant to be removed. For instance, silica particles can
5 be completely removed in 15 seconds while alumina particles may need 30 seconds or more time when using DI water.

The technique is very effective when utilizing only DI water. The technique becomes even more effective when coupled with basic or acidic chemistry (depending on the substrate to be cleaned).

10 **Modes for Carrying Out the Invention**

Most megasonic cleaning processes used extensively in wafer cleaning by the semiconductor, hard drive, and flat-panel display industries are batch cleaning processes that typically take between 10 and 20 minutes, on average, (for 25 wafers
15 cleaned simultaneously in a batch cleaning tank). FIG. 1 shows a schematic cross-sectional elevation view of a typical batch megasonic cleaning apparatus 10 of the prior art. A multiplicity 20 of wafers to be cleaned is held in a cassette 30 which holds wafers 20 in a parallel arrangement inside container 40. Container 40 also holds a liquid cleaning medium 50, which has a liquid surface 55. A megasonic transducer
20 60 transfers megasonic energy 70 through cleaning medium 50 to the surfaces wafers 20.

FIG. 1A shows a magnified detail of a portion of FIG. 1, illustrating schematically by flow lines 100 the slower fluid flow that occurs near the surface of an individual wafer 90 in such a batch process, where the liquid medium flows between wafers.

25 Comparisons between the results obtained using prior art apparatus similar to FIG. 1 as compared with results using the methods and apparatus of the present invention are described below.

Most earlier attempts by various equipment manufacturers at cleaning a single wafer using megasonic cleaning for a short time have not been successful.

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The process of the present invention is capable of accomplishing cleaning in a very short time without the use of any chemicals. This is accomplished by a new process that requires a different geometry and by controlling and using specific process specifications and parameters such as the relative size of the transducer area with
5 respect to the substrate to be cleaned (e.g., semiconductor wafers), distance between wafer and transducer, transducer power and intensity, overflow flow rate, cleaning time, and process temperature. The process steps and the parameters controlled are presented below.

Two alternate configurations for the apparatus are presented in FIGS. 2 and 3. FIG. 2
10 shows a schematic cross-sectional elevation view of a first fast single-wafer megasonic cleaning apparatus 200 made in accordance with the present invention. FIG. 3 shows a schematic cross-sectional elevation view of a second embodiment of a fast single-wafer megasonic cleaning apparatus 200 made in accordance with the present invention. The second embodiment shown in FIG. 2 uses a smaller footprint
15 to reduce the floor area the tool occupies. In both FIGS. 2 and 3, the apparatus 200 includes a container 205 for holding single wafer 90 to be cleaned and for holding the liquid cleaning medium 220, and a megasonic transducer 210 disposed to face the surface of single wafer 90 to be cleaned. Megasonic energy is directed 270 from megasonic transducer 210 toward the surface of single wafer 90 to be cleaned. The
20 apparatus is arranged so that liquid cleaning medium 220 has a free liquid surface 250, and the liquid flow is shown in FIGS. 2 and 3 by flowlines 245 within container 205, by inlet flowlines 240, and by overflow outlet flowlines 260, showing that the liquid cleaning medium 220 overflows the container.

Megasonic transducer 210 has a transducer area between 40% and 100% of the area of
25 the individual substrate 90 to be cleaned. The substrate 90 is positioned parallel to the transducer 210 and spaced apart from megasonic transducer 210 by a predetermined distance. A flow of liquid medium 220 is maintained between the substrate and the transducer, while applying megasonic energy at a suitable frequency of at least 400 kilohertz (kHz). Megasonic transducer 210 may be a conventional piezoelectric
30 transducer capable of operating at a suitable frequency. A conventional supply of

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electrical energy at a suitable frequency is provided to drive the megasonic transducer 210. The megasonic energy applied has a maximum power of at least 400 watts. The megasonic energy applied should be between 20% and 100% of the maximum power and preferably between 50% and 100% when cleaning with DI water alone. The
5 transducer 210 should have a total input intensity (power per unit transducer area) of at least four watts per square centimeter.

For using a transducer area of less than 100% of the area of the individual substrate 90, a relative motion between the individual substrate and the transducer is preferably provided in a direction parallel to the substrate, while performing the fluid-flowing
10 and the megasonic-energy-applying step. The transducer should face at least 40% of the surface area of individual substrate 90 to be cleaned. That is, the major area of the transducer that faces the substrate 90 should have an area that is at least 40% of the major area of one side of the substrate 90 to be cleaned. The distance between the transducer and the individual substrate 90 should be in the range from 1% to 80% of
15 the maximum diameter of substrate 90, or at least a minimum of 1 micrometer or larger away from the substrate. The distance between transducer 210 and the individual substrate 90 is preferably maintained in a range from 1 micrometer to 160 millimeters.

The fluid flowing in the space between the substrate and the transducer is moved at a
20 fluid flow rate sufficient to carry particles away from the substrate before they redeposit on the substrate. The fluid medium 220 flowing in the space between substrate 90 and the transducer 210 is preferably moved at a rate suitable to replace the fluid in the cleaning container 205 in less than or equal to one minute. The overall method for megasonic cleaning of individual substrates 90 with this apparatus thus
25 comprises the steps of: providing a megasonic transducer 210 having a transducer area between 40% and 100% of the area of the individual substrate 90 to be cleaned; disposing the individual substrate 90 substantially parallel to and spaced apart from transducer 210 by a predetermined distance, thereby defining a space between substrate 90 and transducer 210; and flowing a fluid through the space between
30 substrate 90 and transducer 210, while applying megasonic energy to the megasonic

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transducer 210 at a frequency of at least 400 kilohertz (kHz). Optionally, the method can also include the further step of providing relative motion between individual substrate 90 and transducer 210 in a direction substantially parallel to substrate 90, while performing the fluid-flowing and energy-applying step. The fluid-flowing step
5 is preferably performed at a fluid flow rate sufficient to carry particles away from the substrate before they redeposit on the substrate. Preferred process temperatures are in the range 20 °C to 70 °C.

Examples of cleaning results

Experimental data show that the cleaning efficiency obtained using the present
10 invention process (in less than one minute, as little as 15 seconds) is better than that of a batch megasonic cleaning after 13 minutes. One key factor in achieving this improvement using the present invention is in applying to one wafer the same amount of megasonic energy used in a batch process for cleaning 25 wafers.

FIG. 4 is a bar chart illustrating removal efficiency of silica particles using apparatus
15 and methods of the present invention. Vertical axis 400 represents particle removal efficiency, and cleaning time in seconds is plotted along horizontal axis 410. Bar 420 shows the efficiency of removing 0.15 micrometer silica particles with 15 sec. cleaning. Bar 430 shows the efficiency of removing 0.15 micrometer silica particles with 30 sec. cleaning. Bar 440 shows the efficiency of removing 0.15 micrometer
20 silica particles with 45 sec. cleaning.

FIG. 5 is a bar chart illustrating removal efficiency of alumina particles using the present invention, compared with removal efficiency using a method and apparatus of the prior art for various cleaning times. In FIG. 5, vertical axis 500 represents particle removal efficiency, and the processes used are plotted along horizontal axis 510. Bar
25 520 depicts the efficiency of removing alumina particles with 10 min. cleaning using a batch megasonic process of the prior art. Bar 530 depicts the efficiency of removing alumina particles with 20 min. cleaning using a batch megasonic process of the prior art. Bar 540 depicts the efficiency of removing alumina particles with 1 min. cleaning using the fast single-wafer megasonic process of the present invention.

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FIG. 6 is a bar chart illustrating particle counts of alumina particles before and after deposition, and after cleaning by use of the present invention in comparison with particle counts with a prior art method and apparatus. The number of particles larger than 0.1 micrometer is represented by vertical axis 600 of FIG. 6. The horizontal axis 610 represents the process used. Bars 620 represent the number of particles measured before deposition, bars 630 the number of particles after deposition, and bars 640 the number of particles after cleaning. Groups 650 show the results due to cleaning by a batch megasonic cleaning process of the prior art for 10 min. Groups 660 show the results due to cleaning by batch megasonic cleaning process of the prior art for 20 min. Groups 670 show the results due to cleaning by fast single-wafer cleaning process of the present invention for 1 min.

Industrial Applicability

The invention provides a megasonic cleaning process capable of accomplishing cleaning of a single wafer or other substrate in a very short time without the use of any chemicals other than de-ionized water. Apparatus specially adapted for performing the single-wafer megasonic cleaning process has improved efficiency of particle removal. Apparatus made in accordance with the invention is applicable to cleaning processes that require very clean surfaces, especially semiconductor wafer and photomask cleaning processes. The methods of the invention can be used to improve cleanliness of semiconductor wafers, thus increasing the yields and lowering the costs of the semiconductor products formed on the wafers. Similar apparatus suitably arranged can be used for cleaning other planar articles, such as glass or quartz flat panel display substrates, hard-disk-drive disks, and heads.

While the invention has been shown and described in connection with a preferred embodiment, various changes may be made therein without departing from the spirit and scope of the invention as defined in the appended claims. For example, individual cleaning stations as described herein may be combined together in a cluster in arrangements other than those shown. The order of steps of the processes may, of course, be varied. What is claimed is:

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CLAIMS

1. A method for megasonic cleaning of individual substrates, comprising the steps of:
 - 5 a) providing a megasonic transducer having a transducer area between 40% and 100% of the area of the individual substrate to be cleaned;
 - b) disposing said individual substrate substantially parallel to and spaced apart from said transducer by a predetermined distance, thereby defining a space between said substrate and said transducer; and
 - 10 c) flowing a fluid through said space between substrate and said transducer, while applying megasonic energy to said megasonic transducer at a frequency of at least 400 kilohertz.
2. A method as recited in claim 1, further comprising the step of:
 - 15 d) providing relative motion between said individual substrate and said transducer in a direction substantially parallel to said substrate, while performing said fluid-flowing and energy-applying step (c).
3. A method as recited in claim 1, wherein said individual substrate has a major surface area and said substrate is disposed so that said transducer faces at least 40% of said major surface area of said substrate.
- 20 4. A method as recited in claim 1, wherein said substrate has a maximum diameter and said predetermined distance is in a range from 1% to 80% of said maximum diameter.
- 25 5. A method as recited in claim 1, wherein said predetermined distance is in a range from 1 micrometer to 160 millimeters.
- 30 6. A method as recited in claim 1, wherein said megasonic energy applied to said megasonic transducer has a maximum power of at least 400 watts.

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7. A method as recited in claim 6, wherein said megasonic energy is applied to said megasonic transducer with 20% to 100% of said maximum power.
- 5 8. A method as recited in claim 1, wherein said transducer has an area and a total input power and wherein said input power divided by said transducer area is at least four watts per square centimeter.
9. A method as recited in claim 1, wherein said fluid-flowing step (c) of flowing a
10 fluid through said space between said substrate and said transducer is performed at a fluid flow rate sufficient to carry particles away from the substrate before they redeposit on said substrate.
10. A method as recited in claim 1, wherein said fluid-flowing step (c) of flowing a
15 fluid through said space between said substrate and said transducer is performed in a tank having a volume, and at a rate to replace the fluid in said volume in less than or equal to one minute.
11. An apparatus for megasonic cleaning of individual substrates, comprising:
- 20 a) a megasonic transducer having a transducer area between 40% and 100% of the area of the individual substrate to be cleaned;
- b) means for disposing said individual substrate substantially parallel to and spaced apart from said transducer by a predetermined distance, thereby
25 defining a space between said substrate and said transducer;
- c) means for flowing a fluid through said space between said substrate and said transducer; and
- d) means for applying megasonic energy to said megasonic transducer at a frequency of at least 400 kilohertz, whereby energy is transmitted through
30 said fluid across said space between said substrate and said transducer for cleaning said substrate.

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12. Apparatus as recited in claim 11, further comprising:

5 e) means for providing relative motion between said individual substrate and said transducer in a direction substantially parallel to said substrate, while flowing said fluid and applying said megasonic energy.

13. Apparatus as recited in claim 11, wherein said individual substrate has a major surface area and said substrate is disposed so that said transducer faces at least
10 40% of said major substrate surface area.

14. Apparatus as recited in claim 11, wherein said substrate has a maximum diameter and said predetermined distance is in a range from 1% to 80% of said maximum diameter.

15 15. Apparatus as recited in claim 11, wherein said predetermined distance is in a range from 1 micrometer to 160 millimeters.

16. Apparatus as recited in claim 11, wherein said megasonic energy applied to said
20 megasonic transducer has a maximum power of at least 400 watts.

17. Apparatus as recited in claim 16, wherein said megasonic energy is applied to said megasonic transducer with 20% to 100% of said maximum power.

25 18. Apparatus as recited in claim 11, wherein said transducer has a transducer area and a total input power and wherein said input power divided by said transducer area is at least four watts per square centimeter.

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19. Apparatus as recited in claim 11, wherein said fluid-flowing step (c) of flowing a fluid through said space between said substrate and said transducer is performed at a fluid flow rate sufficient to carry particles away from the substrate before they redeposit on said substrate.

5

20. Apparatus as recited in claim 11, wherein said fluid-flowing step (c) of flowing a fluid through said space between said substrate and said transducer is performed in a tank having a volume, and at a rate to replace the fluid in said volume in less than or equal to one minute.

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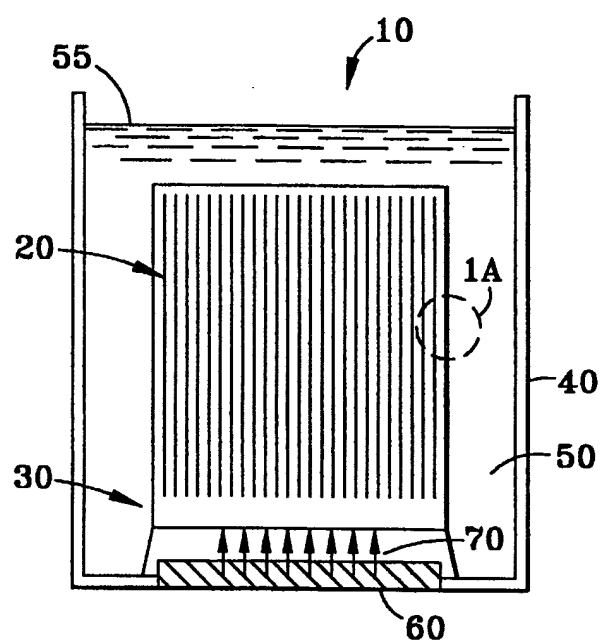
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FIG. 1

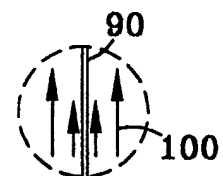


FIG. 1A

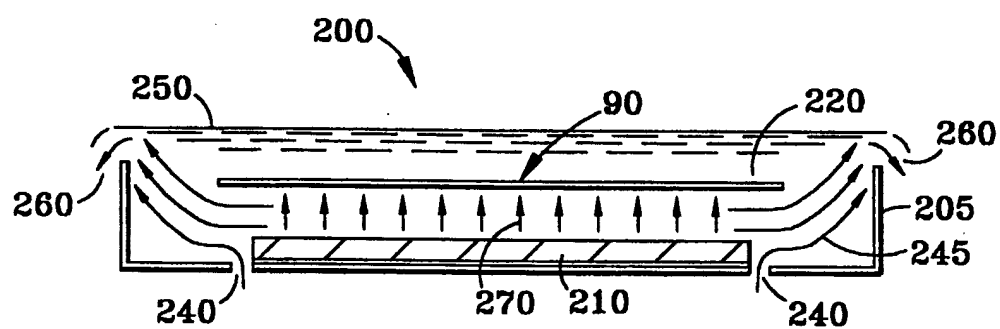


FIG. 2

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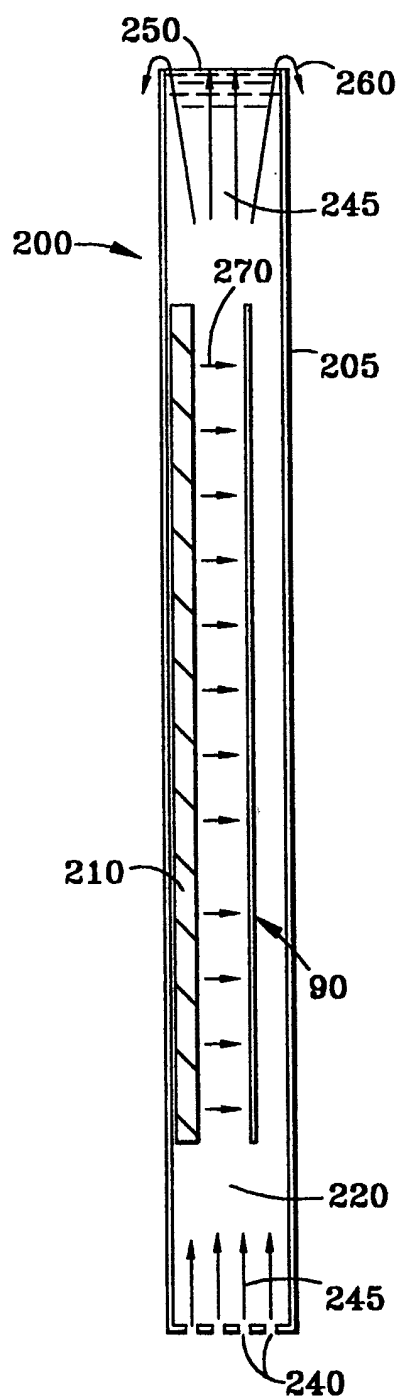


FIG. 3

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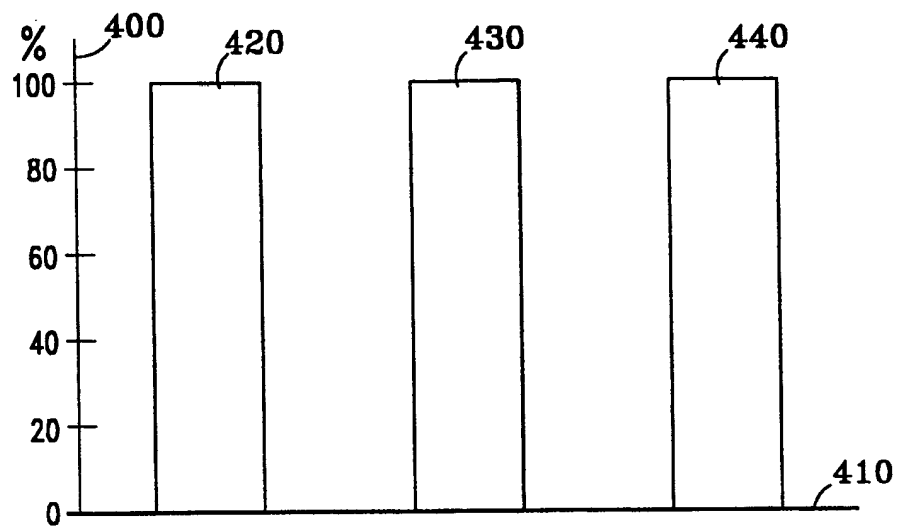


FIG. 4

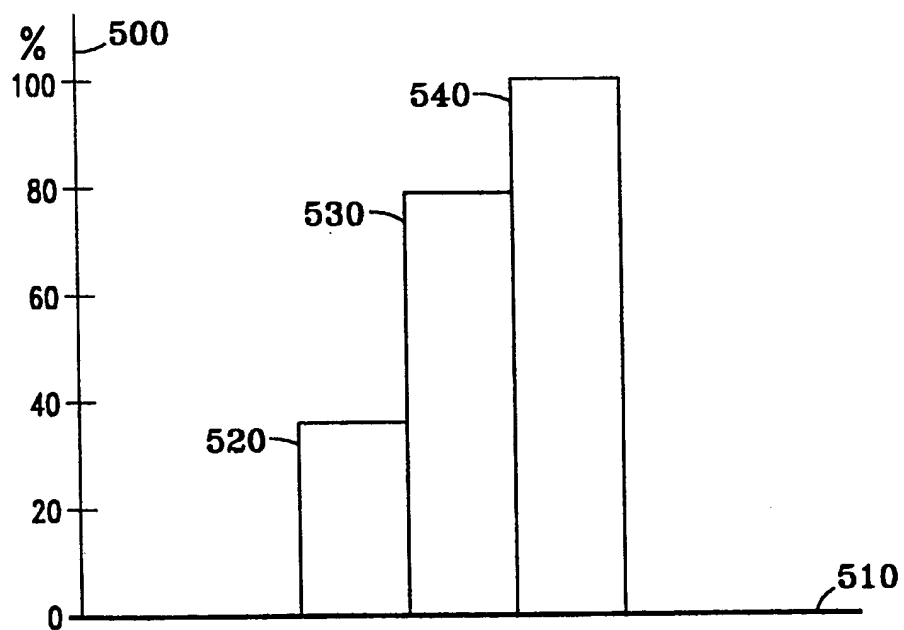


FIG. 5

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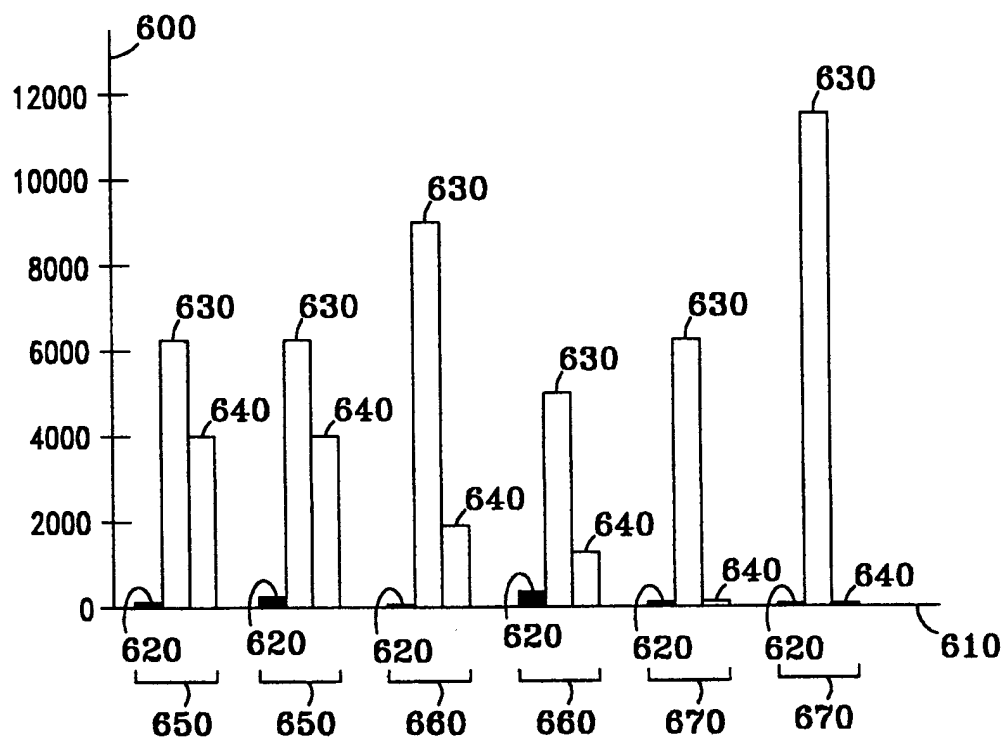


FIG. 6

INTERNATIONAL SEARCH REPORT

Intern al Application No

PCT/US 99/02686

A. CLASSIFICATION OF SUBJECT MATTER
 IPC 6 B08B3/12 H01L21/00

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 6 B08B H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	US 4 401 131 A (LAWSON) 30 August 1983 see column 2, line 7 - column 3, line 68 see figures	1-20
Y	EP 0 603 008 A (SPECIALITY COATING SYSTEMS) 22 June 1994 see column 4, line 36 - column 6, line 32 see figure 1	1-20
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Further documents are listed in the continuation of box C.



Patent family members are listed in annex.

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Date of the actual completion of the international search

7 June 1999

Date of mailing of the international search report

16/06/1999

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
 NL - 2280 HV Rijswijk
 Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
 Fax: (+31-70) 340-3016

Authorized officer

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INTERNATIONAL SEARCH REPORT

International Application No.

PCT/US 99/02686

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	W. KERN: "The Evolution of Silicon Wafer Cleaning Technology" THE ELECTROCHEMICAL SOCIETY, vol. 137, no. 6, June 1990, pages 1887-1892, XP000570485 see page 1888, paragraph 4 -----	1-5, 8-15, 18-20
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